

Amendments to the Specification

Please add the following new heading before paragraph [0001]:

BACKGROUND

Please add the following new heading before paragraph [0004]:

SUMMARY OF THE INVENTION

Please replace paragraph [0004] with the following amended paragraph:

[0004] ~~On this basis, the~~ An object of the present invention is to create a novel circuit arrangement for aircraft engine controllers.

Please replace paragraph [0005] with the following amended paragraph:

[0005] ~~This object is achieved by a circuit arrangement cited in Patent Claim 1.~~ According to the present invention, ~~the~~ a circuit arrangement for aircraft engine controllers ~~is used~~ may be used for providing or generating a bipolar direct current output signal as a function of at least one pulse-width modulated input signal. The circuit arrangement includes at least two driver stages, each driver stage being activatable by a pulse-width modulated input signal, and the or each driver stage being preferably connected to a step-down converter stage in such a way that when a first driver stage is activated by a pulse-width modulated input signal, a first switching element of a step-down converter stage activates a low-pass device of the step-down converter stage and that when a second driver stage is activated by a pulse-width modulated input signal, a second switching element of the step-down converter stage activates the low-pass device of the step-down converter stage.

Please add the following new heading before paragraph [0008]:

BRIEF DESCRIPTION OF THE DRAWING

Please add the following new heading before paragraph [0011]:

DETAILED DESCRIPTION

Please replace paragraph [0016] with the following amended paragraph:

[0016] Second driver stage 21 also includes a transistor Q2. Pulse-width modulated signal D* may be applied to base B_{Q2} of transistor Q2 of second driver stage 20 21 via resistors R8 and R9. A capacitor C3 is connected in parallel to resistor R9. Moreover, additional resistors R10 and R11 are connected in parallel to resistor R9. Resistors R10 and R11 are also connected to emitter E_{Q2} of transistor Q2 of second driver stage 21. Additional resistors R12, R13, R 14, R15 and another capacitor C4 are connected to collector C_{Q2} of transistor Q2 of second driver stage 21 according to Figure 2. The exact wiring of these modules is apparent in Figure 2. Transistor Q2 of second driver stage 21 is designed as a PNP transistor. As is apparent from Figure 2, a supply voltage U_{AUX} for second driver stage 21, which preferably corresponds to the supply voltage of the microprocessor of digital aircraft engine controller 10, is applied to emitter E_{Q2} of transistor Q2.

Please replace paragraph [0021] with the following amended paragraph:

[0021] Diode D1 cooperates with PNP transistor T1 of the first switching element of step-down converter stage 22. Diode D1 is connected to NPN transistor T2 in such a way that anode A_{D1} of diode D1 is connected to emitter E_{T2} of transistor T2 and cathode K_{D1} of diode D1 is connected to collector $T_{T2} \underline{C_{T2}}$ of transistor T2. Diode D2 cooperates with NPN transistor T2 of the second switching element, diode D2 being connected, according to Figure 2, to transistor T1 of the second switching element in such a way that cathode K_{D2} of diode D2 is connected to emitter E_{T1} of PNP transistor T1 and anode A_{D2} of diode D2 is connected to collector C_{T1} of transistor T1. Since the two diodes D1 and D2 are connected to one another in such a way that cathode K_{D1} of diode D1 is connected to anode A_{D2} of diode D2, cathode K_{D1} of diode D1 is furthermore connected to collector C_{T1} of PNP transistor T1 and anode A_{D2} of diode D2 is connected to collector C_{T2} of NPN transistor T2.

On page 7, please amend the heading as follows:

~~PATENT CLAIMS:~~ WHAT IS CLAIMED IS: